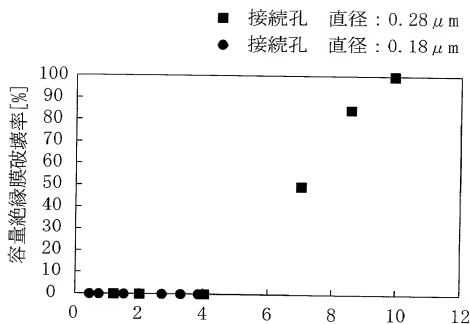
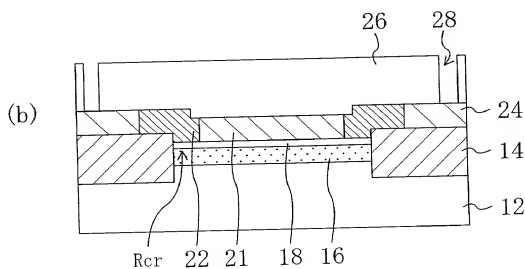
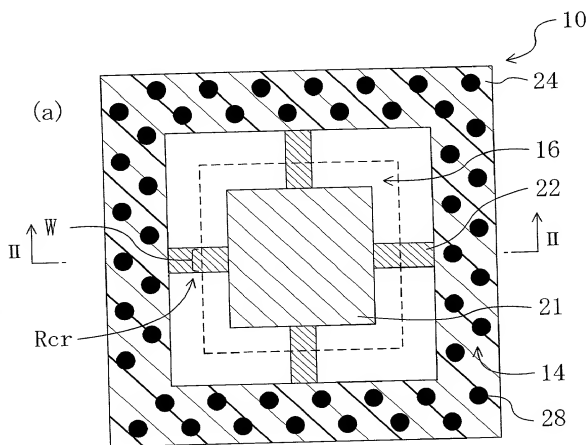


図 1



分離領域と接する容量絶縁膜の境界部Rcrにおける  
引き出し導体膜の幅の総和(L)に対する接続孔に  
おける電極パッドの露出面積の総和(S)の比(S/L)

図2



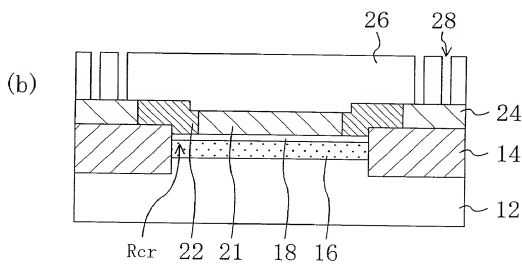
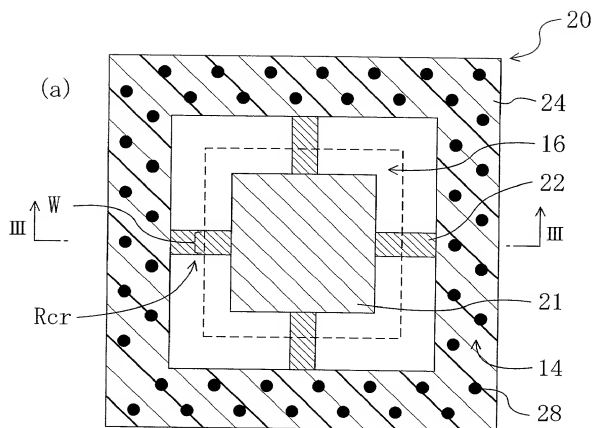
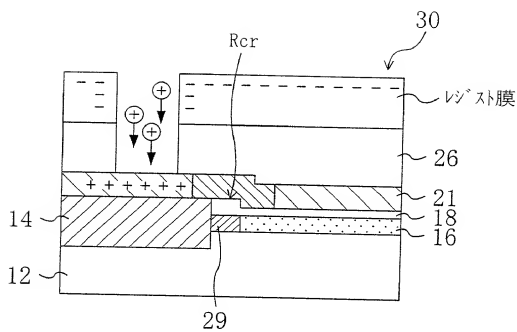


図4



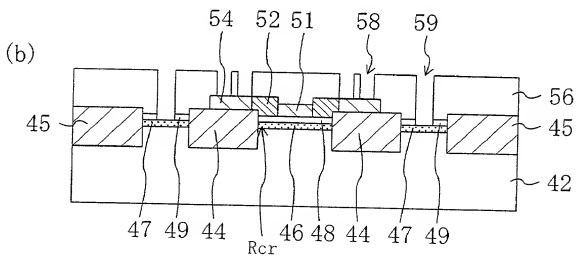
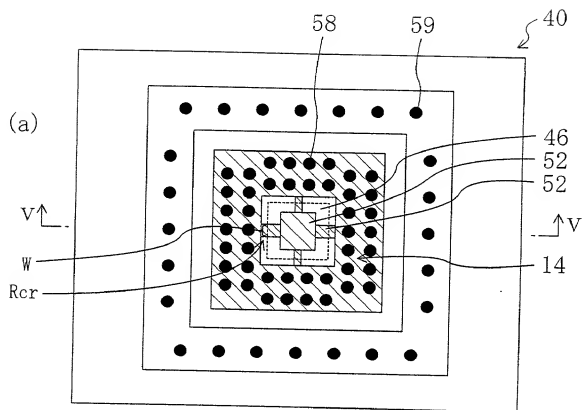


图6

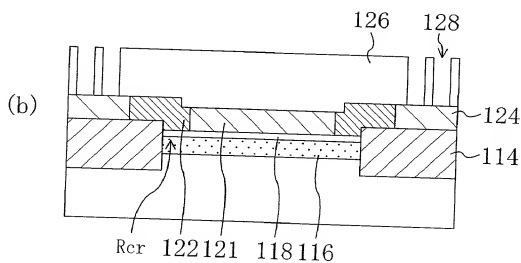
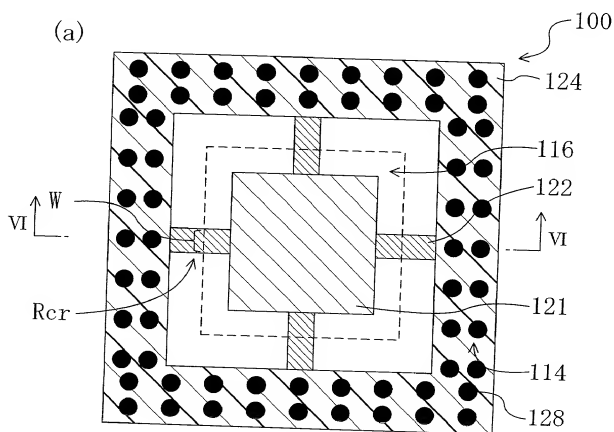


図7

